

**VI TELEFILTER****Filter Specification****TFS 430B****1/5****Measurement condition**

Ambient temperature $T_A$ :	23 °C
Input power level:	0 dBm.
Terminating Impedance at $f_c^*$ :	for input: 240 $\Omega$   6,7 pF
	for output: 210 $\Omega$   7,7 pF

**Characteristics****Remark:**

Reference level for the relative attenuation  $a_{rel}$  of the TFS 430B is the minimum of the pass band attenuation  $a_{min}$ . The minimum of the pass band attenuation  $a_{min}$  is defined as the insertion loss  $a_e$ . The centre frequency  $f_c$  is the arithmetic mean value of the upper and lower frequencies at the 3 dB filter attenuation level relative to the insertion loss  $a_e$ . The nominal frequency  $f_N$  is fixed at 430 MHz without tolerance. The given values for the relative attenuation  $a_{rel}$  and for the group delay ripple have to be reached at the frequencies given below even if the centre frequency  $f_c$  is shifted due to the temperature coefficient of frequency  $TC_f$  in the operating temperature range and due to a production tolerance for the centre frequency  $f_c$ .

<b>D a t a</b>		<b>typ. value</b>	<b>tolerance / limit</b>
<b>Insertion loss</b> (Reference level)	$a_e = a_{min}$	9,2 dB	max. 10 dB
<b>Nominal frequency</b>	$f_N$	-	430 MHz
<b>Centre frequency</b>	$f_c$	430 MHz	
<b>Relative attenuation</b>	$a_{rel}$		
$f_N$	$\dots f_N \pm 2$ MHz	-	max. 1 dB
$f_N \pm 2$	MHz $\dots f_N \pm 3,325$ MHz	-	max. 1,5 dB
$f_N \pm 9$	MHz $\dots f_N \pm 50$ MHz	37 dB	min. 30 dB
$f_N \pm 50$	MHz $\dots f_N \pm 100$ MHz	45 dB	min. 40 dB
<b>Phase Linearity</b>			
$f_N \pm 2$	MHz	0,7 °rms	max. 3 °rms
<b>Operating temperature range</b>		-	- 30 °C ... + 70 °C
<b>Storage temperature range</b>		-	- 35 °C ... + 85 °C
<b>Temperature coefficient of frequency</b>	TC	- 19 ppm/K	
<b>Frequency inversion temperature</b>		+ 25 °C	

\*) The terminating impedances depend on parasitics and q-values of matching elements and the board used, and are to be understood as reference values only. Should there be additional questions, do not hesitate to ask for an application note or contact our design team.

**generated:** \_\_\_\_\_

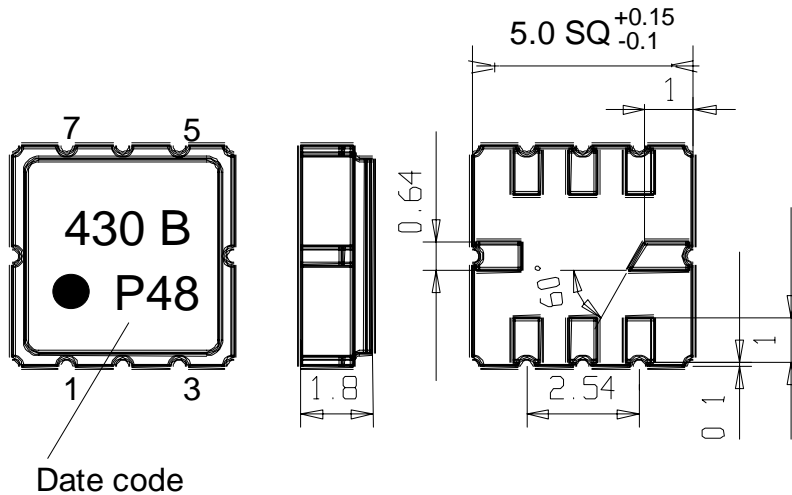
**checked / approved:** \_\_\_\_\_

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**Construction, pin configuration and 50 Ω - matching network**

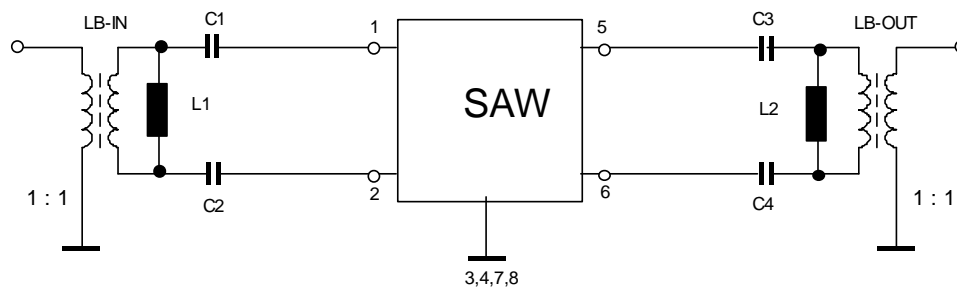
(All dimensions in mm)



Date code

Pin 1	output	Pin 5	input
Pin 2	output	Pin 6	input
Pin 3	ground	Pin 7	ground
Pin 4	package ground	Pin 8	package ground

date code:	year + week
M	2000
N	2001
P	2002

**50 Ohm Test circuit, balanced driven****VI TELEFILTER**

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**Air reflow temperature conditions**

## 1st and 2nd air reflow profile

Name:	pre-heating periods	main-heating periods	peak temperature
Temperature:	150 °C - 170 °C	over 200 °C	255 °C ± 5 °C
Time:	60 sec. - 90 sec.	20 sec. - 25 sec.	

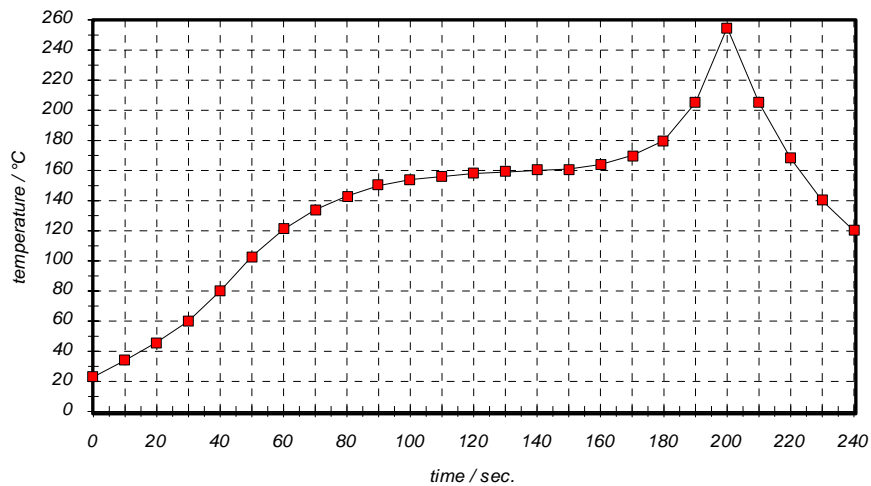
**Chip-mount air reflow profile**

Table for temperature vs. time during the air reflow process

Tolerance of temperatures: ± 5 °C

time / sec.	temperature / °C	time / sec.	temperature / °C
0	23	140	160
10	34	150	161
20	46	160	164
30	60	170	170
40	80	180	180
50	103	190	205
60	121	195	230
70	134	200	255
80	143	205	230
90	150	210	205
100	154	215	180
110	156	220	165
120	158	230	140
130	159	240	120

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**History**

<b>Version</b>	<b>Reason of Changes</b>	<b>Name</b>	<b>Date</b>
1.0	- generation of specification	Pfeiffer	30.07.2002
1.1	- drawing matching networks	Dunzow	27.08.2002
1.2	- adding typical values and terminating impedances	Pfeiffer	26.11.2002